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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Patent of:

Shingo MASUKO

ATTN: Certificates of Correction

Patent Number: US 7,200,924 B2

Issued: April 10, 2007

Attorney Docket No.: 025720-00023

For: METHOD OF PACKAGING ELECTRONIC PARTS

REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

April 30, 2007

Sir:

The undersigned respectfully requests that a Certificate of Correction be issued for the above-identified patent as indicated on the attached Form PTO 1050.

REMARKS

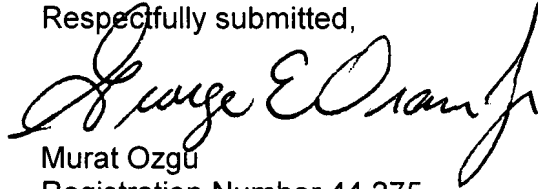
This request is being made in order to correct errors noted in the title page of the above-identified patent. In support of this request, enclosed are copies of the Notice of References Cited (Part of Paper No. 20060109) and the title page of the Letters Patent document showing the requested change.

Certificate
MAY 02 2007
of Correction

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Since the errors in the patent appear to be U.S. Patent and Trademark Office printing errors, it is respectfully submitted that no fee is required. However, in the event that any fees are due with respect to this paper, please charge Counsel's Deposit Account Number 01-2300, referencing Attorney Docket Number 025720-00023.

Respectfully submitted,



Murat Ozgu
Registration Number 44,275

27931

Customer Number 004372
ARENT FOX LLP
1050 Connecticut Avenue, NW
Suite 400
Washington, DC 20036-5339
Telephone: 202-857-6000
Fax: 202-638-4810

MO:vmh

Enclosures: Form PTO 1050 (2)
Notice of References Cited (Part of Paper No. 20060109)
Title Page of Letters Patent Document

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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NUMBER: US 7,200,924 B2

DATED: April 10, 2007

INVENTOR(S): Shingo MASUKO

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title Page

Item (56), References Cited, U.S. Patent Documents, please change

"6,923,958 B2 * 8/2005 Xiang et al." to

-- 5,923,958 A * 7/1999 Chou --

MAILING ADDRESS OF SENDER:

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ARENT FOX LLP

1050 Connecticut Avenue, NW, Suite 400
Washington, DC 20036-5339

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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

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DATED: April 10, 2007

INVENTOR(S): Shingo MASUKO

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FORM PTO 1050 (REV. 3-82)

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**Notice of References Cited**

Application/Control No.

10/777,135

Applicant(s)/Patent Under
Reexamination
MASUKO, SHINGO

Examiner

Donghai D. Nguyen

Art Unit

3729

Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,923,958 A	07-1999	Chou, Li-Kun	438/118
*	B	US-5,991,989 A	11-1999	Onishi et al.	29/25.35
*	C	US-6,268,236 B1	07-2001	Miyawaki, Katumi	438/106
*	D	US-6,310,421 B2	10-2001	Morishima, Koji	310/313R
*	E	US-6,698,084 B2	03-2004	Uchikoba, Fumio	29/601
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	G	US-			
	H	US-			
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	J	US-			
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	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

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(12) **United States Patent**
Masuko

(10) **Patent No.:** **US 7,200,924 B2**
(45) **Date of Patent:** **Apr. 10, 2007**

(54) **METHOD OF PACKAGING ELECTRONIC PARTS**

(56) **References Cited**

U.S. PATENT DOCUMENTS

(75) **Inventor:** **Shingo Masuko, Suzaka (JP)**
(73) **Assignee:** **Fujitsu Media Devices Limited, Yokohama (JP)**
(*) **Notice:** Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 258 days.

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~~6,923,958 B2 * 8/2005 Xiang et al. 438/118~~
5,923,958 A* 7/1999 Chou

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H05K 3/30 (2006.01)

(52) **U.S. Cl.** 29/832; 29/25.35; 29/415;
29/834; 29/835; 257/459; 257/798; 438/108;
438/113; 438/118

(58) **Field of Classification Search** 29/25.35,
29/592.1, 601, 832, 841, 846, 834, 835, 415,
29/417; 257/778, 786, 784, 459, 798; 310/313 R,
310/340, 344, 346, 334; 333/133, 193-196;
361/760, 761; 438/106-118

See application file for complete search history.

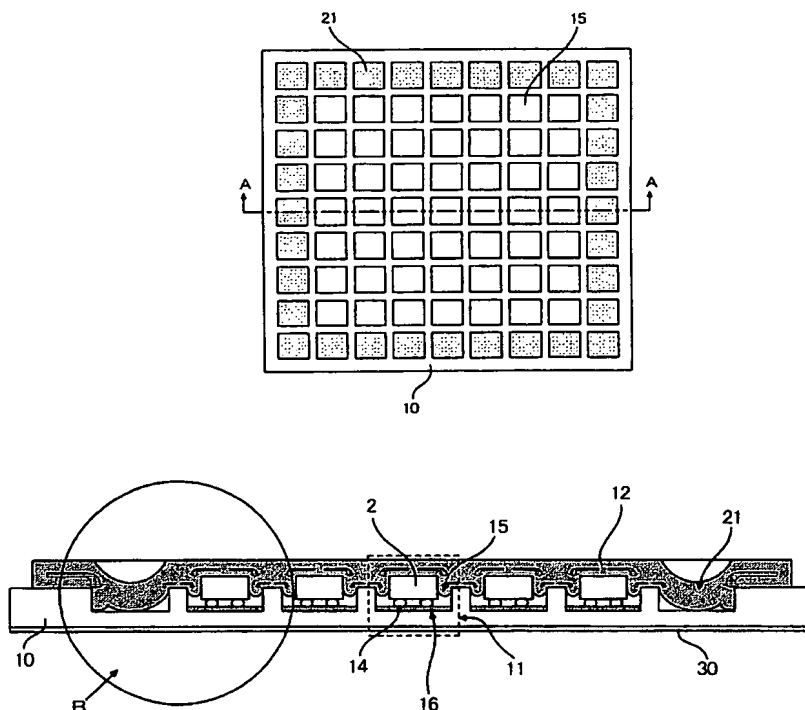
* cited by examiner

Primary Examiner—Minh Trinh
Assistant Examiner—Donghai D. Nguyen
(74) *Attorney, Agent, or Firm*—Arent Fox LLP

(57) **ABSTRACT**

A method of fabricating electronic parts includes the steps of: mounting electronic elements in regular cavities that are two-dimensionally arranged on a baseboard on which dummy cavities are provided so as to surround the regular cavities, and covering a top of the baseboard with a resin sheet.

13 Claims, 9 Drawing Sheets



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